



DEPARTMENT OF ELECTRICAL ENGINEERING

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

South Ambazari Road, Nagpur – 440 010

Maharashtra

Advertisement

Date: 07/04/2017

Call for the position of Junior Research Fellow for the DST SERB Project on

Development of Contactless Energy Exchange System for Autonomous Electric Vehicles

Reference: ECR/2016/002029

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the Department of Science and Technology (DST-SERB), MHRD funded research and development Project on “Development of Contactless Energy Exchange System for Future Autonomous Electric Vehicles”.

Essential Qualification

M.Tech/M.E. (with valid GATE Score) in Power Electronics and Drives/ Power System/ Energy Systems/ Renewable Systems with thesis in the area of power electronics based system. (Students in the last semester of Master degree, can also apply)

Desirable Qualification

Analytical and experimental activities during M.Tech for the development in power electronics based system with concluding understanding; knowledge and experience with technical software/tools (such as ANSYS, COMSOL, PSIM, Matlab) for the simulation studies, and microcontrollers for the implementation of control.

Fellowship

Rs. 25,000p.m. (Consolidated)

Duration

At the beginning assignment will be for one year, which can later be extended up to three years solely based on satisfactory performance.

How to apply

Interested candidates are requested to submit a detailed curriculum vitae

- highlighting and describing the required exposures mentioned in desirable qualification
- at least three reference (phone no. and email id) including B.Tech and M.Tech thesis Supervisors
- abstract of Master's thesis, and
- relevant certificates/evidences

by post and/or through email to the **Principal Investigator (PI)**, by April 25, 2017. Shortlisted candidates will be intimated through email by April 30, 2017. Shortlisted candidates will have to report for interview on May 10, 2017 (10:30AM) in Electrical Engineering Department, VNIT Nagpur.

Note

1. No TA/DA will be paid for to appear in interview.
2. Decision of Director VNIT Nagpur will be the final in all respect.

Correspondence address: PI

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**DIRECTOR
(VNIT, NAGPUR)**