VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY NAGPUR440010
CENTER FOR VLSI AND NANOTECHNOLOGY

Requirement of Contract Staff for the Chips to System Design -Special Manpower development Program (C2SD-SMDP) sponsored by Department of Electronics and Information Technology.

1. Lab Engineer

Qualification	Salary
Candidate should hold M. Tech/M.E. in VLSI Design/Microelectronics from a recognized university. The candidates having experience in numerical simulations, circuit, Layout design and successfully completing a project in areas related will be preferred. Having working knowledge of Linux, software tools related to VLSI is advantageous.	The remuneration will be Rs. 34000 per month.

2. SOC Engineer

Qualification	Salary
Candidate should hold a M.Tech. Degree in VLSI Design/Microelectronics from a recognized university. Having a working knowledge of Linux, VLSI software tools is advantageous. Final year M. Tech candidates who completed their course work can also apply provided they can submit the copy of their certificates of completion of requirements for degree by August 31 2017.	The remuneration will be Rs.30000 per month.

3. Project Associate

Qualification	Salary
Essential: B. Tech in Electronics / Electronics and Comm. or equivalent with first class or Equivalent. Candidates with experience of VLSI/Chip Design and test/ Embedded System, Linux and Programming language and FPGA implementation related projects will be preferred.	The remuneration will be Rs. 20000 per month.

4. Research Associate

Qualification	Salary
Essential: B. Tech in Electronics /Computer Science & Engg./ Electrical Engineering or M.Sc. in Electronics/Physics or Equivalent with first class or Equivalent. Candidates with experience in VLSI/Chip Design and test/ Embedded System, Linux and Programming language and FPGA implementation related projects will be preferred.	The remuneration will be Rs. 20000 per month.

Candidates having qualifying GATE score will be preferred for all these posts. The duration of the post will be initially for **one year** and may be extended further subject to satisfactory performance and provisions under the Project. The posts will be coterminous with the duration of the project. Interested candidates may apply on or before 05th April 2017 with a copy of resume mentioning Educational Qualifications Professional Experience (if any), contact details including email and phone numbers. Please mention the "C2SD SMDP Project" on top of the envelop and mail it to "Registrar, VNIT, South Ambazari Road Nagpur 440010.

Note: Mere fulfillment of qualifications/experience requirements does not entitle a candidate to be called for the interview. Candidates will be shortlisted based on their academic performance/professional experience and only the shortlisted candidates will be called for an interview. Selected candidate will be intimated by email. No correspondence whatsoever will be entertained from candidates regarding reasons for not being called for interview, conduct and result of interview. No TA/DA is permissible for attending the interview. The Institute reserves the right to: (a) conduct written/trade tests for such posts wherever if the circumstances so warrant (b) not filling any of the advertised positions (c) fill consequential vacancies arising at the time of interview from available candidates. The number of positions is thus open to change.

The decision of the **Director, VNIT Nagpur** will be final in all respect.

Registrar