



विश्वेश्वरय्या राष्ट्रीय प्रौद्योगिकी संस्थान
नागपूर - 440 010 (भारत)

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY,
NAGPUR - 440 010 (INDIA)

Centre for Distance Engineering Education Programme (C-DEEP)

WEB: www.vnit.ac.in

Email: dep@vnit.ac.in

Dr. B. S. Umre:

RC Coordinator

bsumre@eee.vnit.ac.in

bsumre@rediffmail.com

Mobile No.: 09423102827

27.10.2016

**TWO-WEEK ISTE STTP ON CMOS, MIXED SIGNAL AND
RADIO FREQUENCY VLSI DESIGN**

To enhance the teaching effectiveness in core engineering subjects, IIT Kharagpur has started a new initiative under National Mission on Education through ICT supported by MHRD. As a part of this initiative, IIT Kharagpur is organizing a two-week ISTE sponsored workshop on “**CMOS, Mixed Signal and Radio Frequency VLSI Design**” for teachers across the country via distance – education mode. **VNIT (RC ID 1128)** as an active ISRO - EduSat remote centre is already part of this activity and has organized workshops in the past.


This time the workshop is decided to be held face to face during **30th January – 4th February, 2017** under Blended MOOCs (Massive Open Online Courses) model. Live lectures will be given by IIT faculty. The participating teachers will attend them at our remote centre with live online interactive participation. Tutorials will be conducted at the same place. As enrolment will be **strictly online** and no other applications will be entertained, kindly enroll selecting **Visvesvaraya National Institute of Technology (VNIT) (RC ID 1128)**, Nagpur as remote centre at this website <http://www.nmeict.iitkgp.ernet.in/>

The participants must be a faculty in Electronics and Communication Engineering / Electrical Engineering /Computer Science and Engineering /Instrumentation Engineering /Information Technology /Physics with basic VLSI knowledge/ Mathematics with basic VLSI knowledge/ Material Science with basic VLSI knowledge/ related Departments and Centres. The faculty member(s) who are teaching or planning to teach CMOS, Mixed Signal and Radio Frequency VLSI Design or related subjects to UG or PG level are also eligible.

In this workshop,

1. The participating teachers will complete the equivalent of two-week full time work online, spread over 6 physical weeks where video lectures and assignments will be uploaded beforehand.
2. After completing the online assignments spread over 4 to 5 weeks the participants will assemble at the selected Remote Centers for 6 days face to face interaction and lecture sessions through A-VIEW and will complete team assignments, tutorials, quizzes etc.
3. Offline assignments will be uploaded and the participants will have to complete these assignments within a stipulated time.
4. There will also be a system of students' feedback in the Main STTP.

The last date for registration is 28th November, 2016.


(Dr. B. S. Umre)
RC Coordinator