



# DEPARTMENT OF ELECTRICAL ENGINEERING

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

South Ambazari Road, Nagpur – 440 010 Maharashtra

Advertisement

Date: 28/12/2018

**Call for the position of Junior Research Fellow for the collaborative Project between  
BARC, Mumbai and VNIT, Nagpur**

## **Modelling, Stability and Fault Analysis of BLDC type Multi-Machine System**

**Reference:**

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) on contract basis for limited period of time for **BARC, Mumbai** funded **collaborative Project between BARC, Mumbai and VNIT, Nagpur** on “**Modelling, Stability and Fault Analysis of BLDC type Multi-Machine System**”.

### **Essential Qualification**

M.Tech/M.E. (GATE qualified) Power System / Power Electronics and Drives / Energy Systems.

### **Desirable Qualification**

Analytical and experimental activities during M.Tech with concluding understanding; knowledge and experience with technical software/tools (MATLAB/LABVIEW/PSCAD) for the simulation studies. Knowledge of power system dynamics, fault studies and machine modelling.

### **Fellowship**

First two years Rs. 25,000p.m. (+HRA:20%) and third year Rs. 28000 (+HRA:20%)

### **Duration**

Maximum duration of the project is three years. Initially the contract will be for six months, which can later be extended every six months till the completion of project subject to satisfactory performance.

### **How to apply**

Interested candidates are requested to submit application in proper format (*available online on institute website*) and detailed curriculum vitae through email

- highlighting and describing the required exposures mentioned in desirable qualification
- at least three reference (*phone no. and email id*) including B.Tech and M.Tech thesis Supervisors
- abstract of Master's thesis, and
- relevant certificates/evidences

by post and/or through email to the **Principal Investigator (PI)**, by **10<sup>th</sup> January, 2019**. Shortlisted candidates will be intimated through email by **14<sup>th</sup> January, 2019**. Shortlisted candidates will have to report for interview on **24<sup>th</sup> January, 2019 (10:30AM)** in Electrical Engineering Department, VNIT Nagpur.

### **Note**

1. No TA/DA will be paid to appear for interview.
2. Decision of Director VNIT Nagpur will be the final in all respect.

**Encouragement for pursuing higher degree:** Student selected as JRF is eligible to apply for Ph.D. program provided the candidate clears the selection criteria of the Institute.

### **Correspondence address: (PI)**

Dr. Sunil Bhat, Associate Professor  
Department of Electrical Engineering,  
VNIT Nagpur, South Ambazari Road,  
Nagpur – 440 010, Maharashtra  
Phone: 9049800298 (M) +917122801128 (O)  
e-mail: [ssbhat@gmail.com](mailto:ssbhat@gmail.com)

**DIRECTOR (VNIT, NAGPUR)**