

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

South Ambazari Road, Nagpur - 440 010

Maharashtra

Advertisement: Department of Electrical Engineering, VNIT Nagpur

Date: 21/09/2021

Call for the position of Junior Research Fellow for the DST funded Project on High Frequency Planar Magnetics for Renewable Energy System and Electric Vehicle Battery Charging under Solar Energy Research and Development (SERD-2019) programme.

Reference: DST/TMD/CERI/RES/2020/39(G)

Applications are invited from motivated and eligible candidates for two numbers of Junior Research Fellow (JRF) for the High Frequency Planar Magnetics for Renewable Energy System and Electric Vehicle Battery Charging under Solar Energy Research and Development (SERD-2019) programme funded by Department of Science and Technology, Government of India. JRFs are required to work on the topics:

- 1) Design, development and fabrication of Electric Vehicle Charger using High Frequency Planar Magnetics and Wide Band Gap Devices.
- 2) Integrated control of electric vehicle charging system.

Essential Qualification

M.Tech/M.E. (*GATE qualified in past*) in Power Electronics and Drives/ Power System/ Energy Systems and related specialization with thesis in the area of Power Electronics/Power System/Electric vehicles/renewables.

Desirable Qualification

Analytical and experimental activities during M.Tech for the development in power electronics/power system with renewable sources having concluding understanding; knowledge of PCB design and experience with technical software/tools (*such as PSIM, MATLAB, Code-composer*) for the simulation studies, and microcontrollers for the implementation of control.

Age Limit:

30Yrs (preferably).

Fellowship

First two years (@ 31000/- + 16% HRA pm) and next one Year (@35000/- + 16% HRA)

Duration

At the beginning assignment will be for the Six months, which can be extended in every six-month based on the performance and approval by the competent authority till the maximum duration of the project.

Note: Student will have opportunity to apply for PhD Program with Fellowship from the project

How to apply

Interested candidates are requested to submit application in proper format (available online on institute website) and detailed curriculum vitae

- highlighting and describing the required exposures mentioned in desirable qualification
- at least two reference (phone no. and email id) including B.Tech and M.Tech thesis Supervisors
- abstract of Master's thesis, and
- relevant certificates/evidences
- Transcript/Marksheet

through email to the **Principal Investigator**, by **October 12**, **2021**. Shortlisted candidates will be intimated through email by **October 15**, **2021**. Shortlisted candidates will have to report for the *online/in-person interview* and written test on **October 21**, **2021** (**10:00AM**) to be conducted by Electrical Engineering Department, VNIT Nagpur.

Note:

- 1. No TA/DA will be paid for to appear in interview.
- 2. Decision of Director VNIT Nagpur will be the final in all respect.

Correspondence address: Principal Investigator

Prof. H.M Suryawanshi, FNAE, FIEEE / Dr. Ritesh Kumar Keshri

Department of Electrical Engineering, VNIT Nagpur

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