



DEPARTMENT OF ELECTRICAL ENGINEERING
VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY
South Ambazari Road, Nagpur – 440010
Maharashtra

Date: 18/01/2022

Call for the position of Two Junior Research Fellows for the DST India-European Union (EU) Consortium Research Project “Sustainable Energy system for Achieving Novel Carbon Neutral Energy Communities (SUSTENANCE)”

Applications are invited from self-motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the **Department of Science and Technology (DST) India – European Union (EU) Consortium Project** on “Sustainable Energy system for Achieving Novel Carbon Neutral Energy communities (SUSTENANCE)” with primary focus on “**Development of Smart Solid State Transformer for Electric Vehicle Charging Infrastructure**”.

Essential Qualifications:

1. M.Tech/M.E.in Electrical Engineering Department with specialization in Power Electronics/Electrical Drives/Power System/Energy System/Control System or equivalent.
2. Candidate should have qualified GATE in the past.

Desirable Qualifications:

1. Age: Preferably below 35 years.
2. Candidate should have experimental knowledge in the **hardware and controller design of power electronic converters**. First division in UG and PG will be given preference.
3. Basic knowledge of **MATLAB, PSIM, PCB Design Software** and **Microcontroller/Digital Signal Controller/FPGA**.
4. **Good analytical ability** and competent in technical writing skills.

Fellowship:

First two years Rs. 31,000 p.m. (Consolidated) and Third year Rs. 35,000 (consolidated). HRA will be given as per rules.

Duration:

Maximum duration of the project is 42 Months. Initially, appointment will be made for six months, which can be extended later for every six months till the completion of project subject to approval of Director, VNIT. The extension shall be solely based on satisfactory performance.

How to apply:

Interested candidates are requested to submit duly filled and signed application form in the format (available online on the institute website www.vnit.ac.in) and detailed Curriculum Vitae with skills and achievements through **email** and **hard copy** to the Principal Investigator before the deadlines. Along with the application, candidates have to send two letters of recommendation from the person well acquainted with candidate’s academic and research abilities.

Note:

1. Selection process consists of Written Test and Interview.
2. Candidates selected for this position will have the opportunity to register for regular Ph. D. program, in July 2022, provided they satisfy the eligibility criteria for the same.
3. Decision of The Director, VNIT Nagpur will be the final in all respect.

Correspondence address (Principal Investigator):

Dr. Pradyumn Chaturvedi,
Assistant Professor, Department of Electrical Engineering,
VNIT Nagpur South Ambazari Road, Nagpur – 440010, M.S., India
Phone: +91 9826585016 (Mob), +917122801339 (Off), e-mail: pradyumn.c@eee.vnit.ac.in

Important Dates:

Submission deadline for Application Form (e-mail & Hard Copy)	17 Feb, 2022
Intimation to Shortlisted Candidates (via e-mail)	18 Feb, 2022
Written Test (online)	24 Feb, 2022
Interview (online)	25 Feb, 2022

DIRECTOR (VNIT, NAGPUR)