



VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY, NAGPUR

CENTER FOR VLSI AND NANOTECHNOLOGY

ADVERTISEMENT FOR JRF RECRUITMENT

Applications are invited for the Junior Research Fellow (JRF) position (on contract basis) to work under DRDO sponsored project entitled “**Design and Computational Optimization of Group III-nitride High Power Laser Source**”.

Eligibility: Candidate should hold a M.Tech. Degree in relevant areas such as Microelectronics /Photonics/VLSI Design etc. or M.Sc. degree in relevant areas such as Physics/Electronics/Photonics etc. with a good academic background and sound knowledge in Optoelectronics/Photonics/Nanofabrication Technologies is desirable.

| S.No. | Designation and Qualification | Remuneration per month |
|-------|---|---------------------------------------|
| 1. | Junior Research Fellow (JRF) : 01 M.Tech. in Micro/Nanoelectronics/VLSI Design or M.Sc. in Physics with a minimum of First Class at Master’s level. (GATE/NET qualification in relevant area is desirable) | Rs. 31,000/- (Consolidated) |

Duration of the project: 2 years (maximum) or until tenure of the project (whichever is higher).

Mode of Selection: By Interview

NOTE: Candidates having qualifying GATE/NET score will be preferred. The duration of the post will be initially for six months and may be extended every six months for two years, further subject to satisfactory performance and provisions under the Project. The post is entirely temporary in nature and will be terminated with the completion of the project. **The candidate selected for the position of JRF will have the option to register for PhD program subjected to satisfying VNIT’s norms for registration** (criteria can be found at: <https://vnit.ac.in/admission/#ffs-tabbed-17>).

Interested candidates are requested to submit duly filled application form in the format (available online on the institute website www.vnit.ac.in) along with self-attested enclosures (CV, Mark Sheets, Degree certificates, GATE score card, Caste certificate if applicable and any other relevant documents) as a single pdf file through email and post (hard copy) to the Principal Investigator before the deadline.

Dr. Nikhil Deep Gupta, Assistant Professor (Principal Investigator)
Centre for VLSI and Nanotechnology, V.N.I.T. South Ambazari Road, Nagpur – 440010, Maharashtra
e-mail: nikhildeepgupta@cvn.vnit.ac.in; nikhildeep.gupta@gmail.com

Applications should reach on or before **30 December 2022**.

Short-listed candidates will be intimated by e-mail only. Mere fulfillment of qualifications/experience requirements does not entitle a candidate to be called for the interview. Candidates will be shortlisted based on their academic performance/professional experience and only the selected candidate will be intimated by email. No TA/DA is permissible for attending the interview. The Institute reserves the right to: (a) conduct written/trade tests for such posts wherever if the circumstances so warrant (b) not filling any of the advertised position.

Notification is also available on institute webpage www.vnit.ac.in

The decision of the Director, VNIT, Nagpur will be final in all respect.

REGISTRAR, VNIT