



# DEPARTMENT OF ELECTRICAL ENGINEERING

VISVESVARAYA NATIONAL INSTITUTE OF TECHNOLOGY

South Ambazari Road, Nagpur – 440 010

Maharashtra

Advertisement

Date: 21/02/2024

## Call for the position of Junior Research Fellows for the DST-SERB Sponsored Research Project

**Name of the Centre to be established:** SERB Centre for Energy Transformation and Storage

**Name of Scheme:** Intensification of Research in High Priority Areas (IRHPA)

**Title of Project:** “Sustainable Technology for Smart Off-Grid Renewable Energy Transformation and Electrochemical Storage”

**Number of Positions:** One

**Job Profile:** To design, develop and deploy the ac/dc microgrid with PV and Storage support based on Smart Solid State Transformer components.

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellows (JRFs) for the above mentioned Department of Science and Technology (DST-SERB) sponsored project.

### Essential Qualifications:

M. Tech/M. E. in Electrical Engineering Department with specialization in Power Electronics/Electrical Drives/Power System/Energy System/Control System or in related areas.

### Desirable Qualifications:

1. Qualified in GATE/CSIR-UGC NET or selected through national level exam conducted by central govt deptts and their agencies and institutions such as DST, DBT, DAE, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.
2. Candidate should have experimental knowledge in the **hardware/control design of power electronic converters**. First division in UG and PG will be given preference.
3. Proficient in **MATLAB, PSIM, PCB Design Software** and **Microcontroller/Digital Signal Controller**.
4. Good analytical ability and competent in English writing,

**Fellowship:** First two years Rs. 31,000 p.m. (consolidated) and Third year Rs. 35,000 (consolidated).

### Accommodation:

Suitable accommodation will be provided in the institute hostel on payment basis.

### Duration:

Maximum duration of the project is **Three Years**. Initially, appointment will be made for six months, which can be extended later for every six months till the completion of project subject to approval of Director, VNIT. The extension shall be solely based on satisfactory performance.

### How to apply:

Interested candidates are requested to submit duly filled and signed application form in proper format (available online on institute website) and detailed Curriculum Vitae through **email**. Please stick to the deadlines mentioned below. Along with the application, candidates have to send (i) Two Reference Letters, and (ii) Maximum One Page details on the skills and achievements.

**Selection Procedure:** Shortlisted candidates have to attend the **Written Test and Interview** on the date mentioned below. Written Test will be from the courses of Power Electronics, Power System, Network Theory and Control System.

### Note:

1. No TA/DA will be paid to appear for written test/interview.
2. **Candidate selected for this position can also have opportunity to register for regular PhD program provided they satisfy the eligibility criteria for PhD program of VNIT.**
3. Decision of Director VNIT Nagpur will be the final in all respect.

### Correspondence address (Principal Investigator):

Dr. Pradyumn Chaturvedi, Associate Professor, Department of Electrical Engineering, VNIT Nagpur, South Ambazari Road, Nagpur – 440010, M.S., India Phone: +91 9826585016 (Mob), +91 712 2801339 (Off)  
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### Important Dates:

Submission deadline for Application Form (e-mail & Hard Copy)	18 Mar, 2024
Intimation to the Candidates for Written Test and Interview	19 Mar, 2024
Written Test and Interview	27 Mar, 2024

**DIRECTOR (VNIT, NAGPUR)**